1062071.2925h001

FIG.1

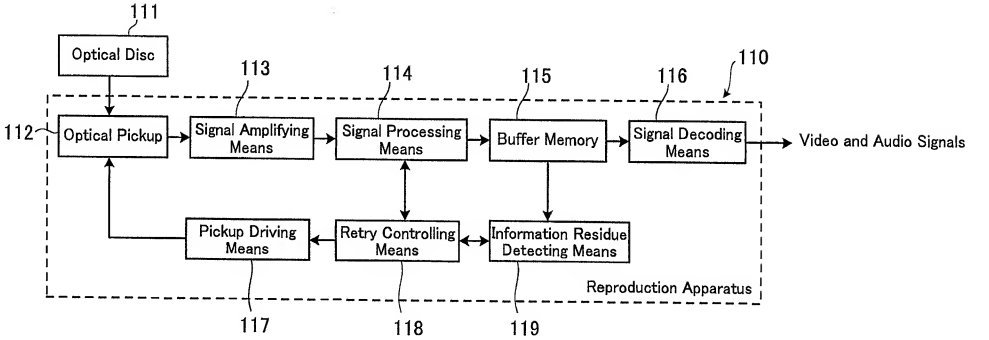

FIG.2

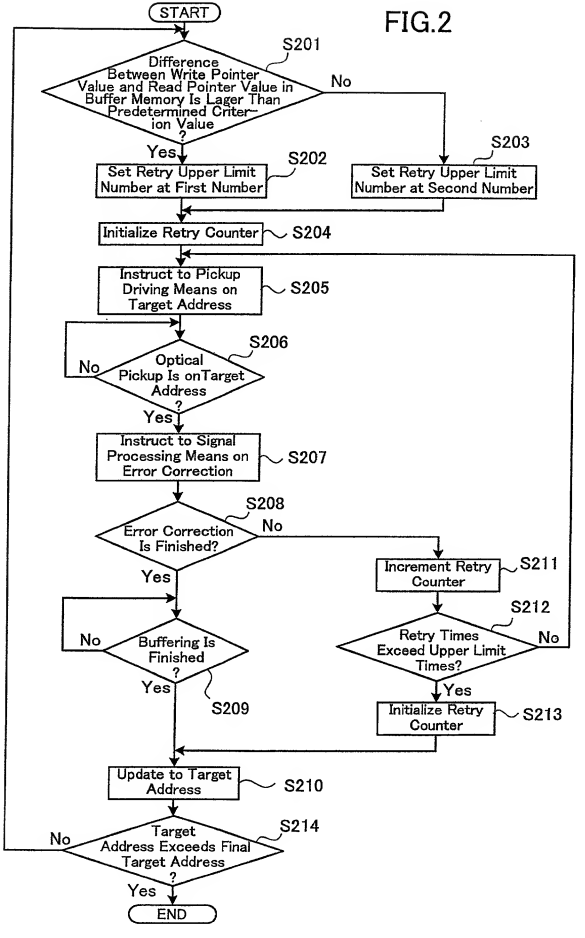
START
S201
Difference Between Write Pointer Value and Read Pointer Value in Buffer Memory Is Lager Than Predetermined Criterion Value ?
No
Yes
S202
Set Retry Upper Limit Number at First Number
S203
Set Retry Upper Limit Number at Second Number
Initialize Retry Counter
S204
Instruct to Pickup Driving Means on Target Address
S205
S206
Optical Pickup Is onTarget Address ?
No
Yes
Instruct to Signal Processing Means on Error Correction
S207
S208
Error Correction Is Finished?
No
Yes
Increment Retry Counter
S211
S212
Retry Times Exceed Upper Limit Times?
No
Yes
Initialize Retry Counter
S213
Buffering Is Finished ?
No
Yes
S209
Update to Target Address
S210
S214
Target Address Exceeds Final Target Address ?
No
Yes
END

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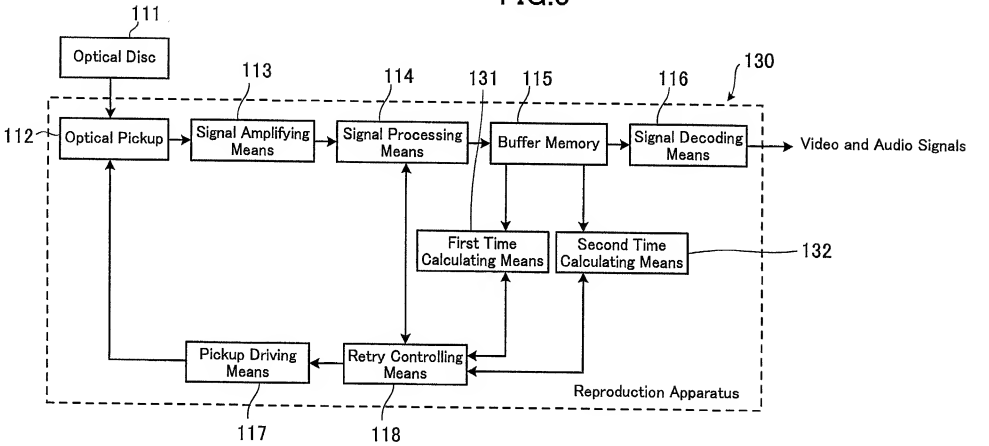
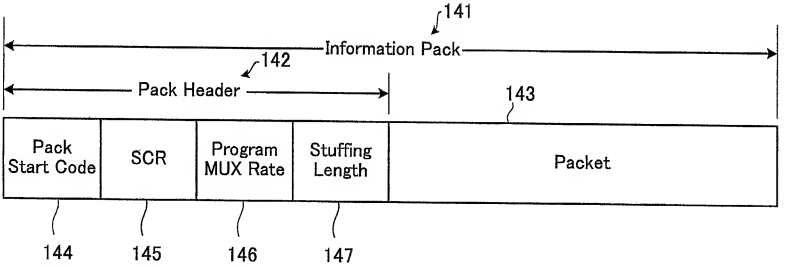
FIG.3



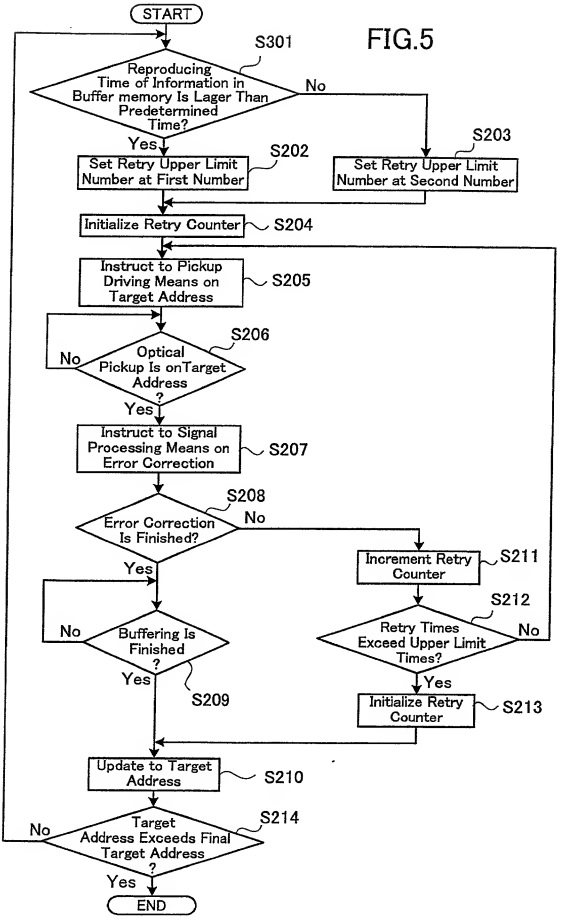
TO62OT:29ESh:OO

FIG.4



Machine Translation

FIG.5



TOSHIBA SCANSHOT

FIG.6

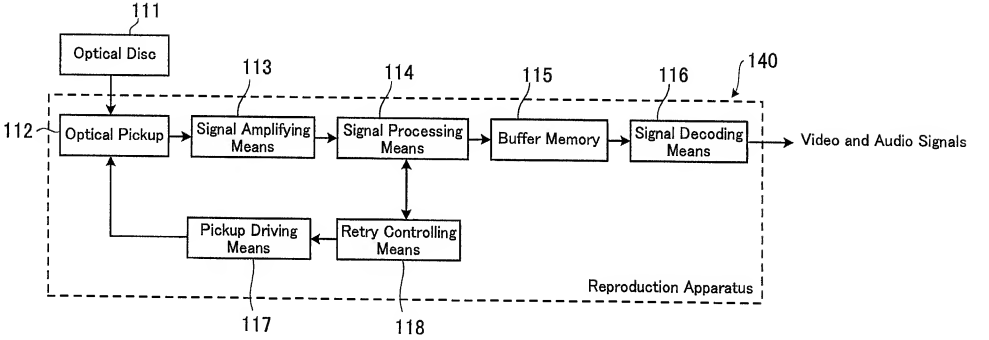


FIG.7

Data Name	Content
FWDI 240	+240 VOBU First Target Address
FWDI 120	+120 VOBU First Target Address
FWDI 60	+60 VOBU First Target Address
FWDI 20	+20 VOBU First Target Address
FWDI 15	+15 VOBU First Target Address
FWDI 14	+14 VOBU First Target Address
FWDI 13	+13 VOBU First Target Address
FWDI 12	+12 VOBU First Target Address
FWDI 11	+11 VOBU First Target Address
FWDI 10	+10 VOBU First Target Address
FWDI 9	+9 VOBU First Target Address
FWDI 8	+8 VOBU First Target Address
FWDI 7	+7 VOBU First Target Address
FWDI 6	+6 VOBU First Target Address
FWDI 5	+5 VOBU First Target Address
FWDI 4	+4 VOBU First Target Address
FWDI 3	+3 VOBU First Target Address
FWDI 2	+2 VOBU First Target Address
FWDI 1	+1 VOBU First Target Address
FWDI Next	Next VOBU First Target Address
BWDI Prev	Before VOBU First Target Address
BWDI 1	−1 VOBU First Target Address
BWDI 2	−2 VOBU First Target Address
BWDI 3	−3 VOBU First Target Address
BWDI 4	−4 VOBU First Target Address
BWDI 5	−5 VOBU First Target Address
BWDI 6	−6 VOBU First Target Address
BWDI 7	−7 VOBU First Target Address
BWDI 8	−8 VOBU First Target Address
BWDI 9	−9 VOBU First Target Address
BWDI 10	−10 VOBU First Target Address
BWDI 11	−11 VOBU First Target Address
BWDI 12	−12 VOBU First Target Address
BWDI 13	−13 VOBU First Target Address
BWDI 14	−14 VOBU First Target Address
BWDI 15	−15 VOBU First Target Address
BWDI 20	−20 VOBU First Target Address
BWDI 60	−60 VOBU First Target Address
BWDI 120	−120 VOBU First Target Address
BWDI 240	−240 VOBU First Target Address

FIG.8

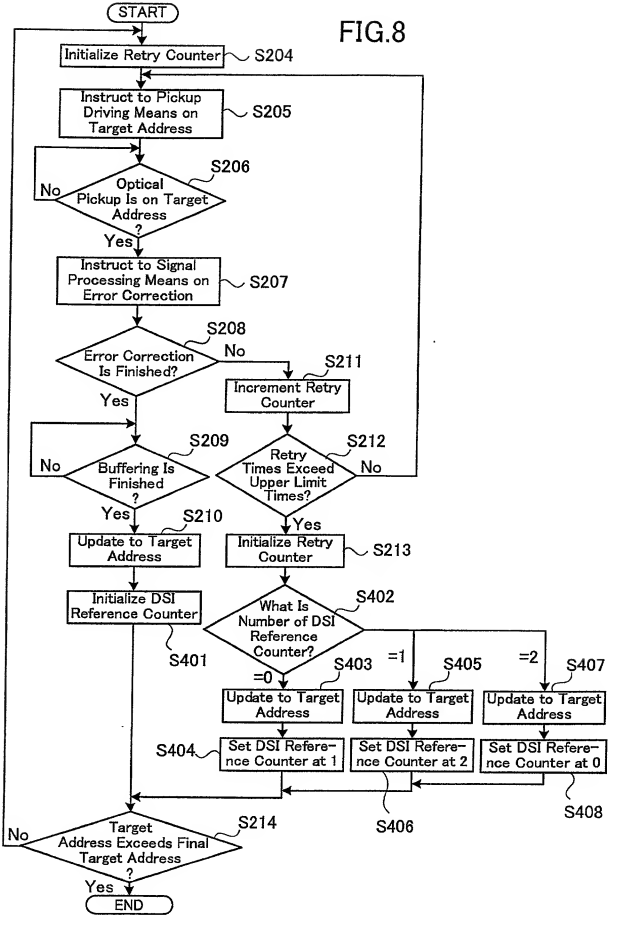
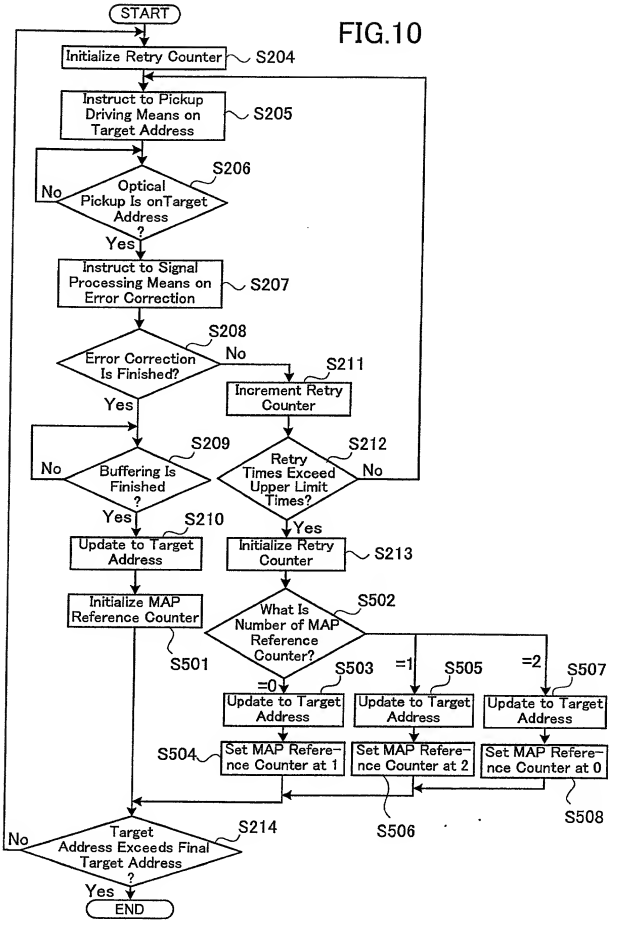

START
Initialize Retry Counter
S204
Instruct to Pickup Driving Means on Target Address
S205
S206
Optical Pickup Is on Target Address ?
No
Yes
Instruct to Signal Processing Means on Error Correction
S207
S208
Error Correction Is Finished?
No
S211
Increment Retry Counter
Yes
S209
Buffering Is Finished ?
No
Yes
S212
Retry Times Exceed Upper Limit Times?
No
Yes
S210
Update to Target Address
Initialize Retry Counter
S213
Initialize DSI Reference Counter
S401
S402
What Is Number of DSI Reference Counter?
=0
S403
=1
S405
=2
S407
Update to Target Address
Update to Target Address
Update to Target Address
S404
Set DSI Reference Counter at 1
Set DSI Reference Counter at 2
Set DSI Reference Counter at 0
S406
S408
S214
Target Address Exceeds Final Target Address ?
No
Yes
END

FIG.9

Data Name	Content
TMU	Time Unit
MAP_EN_Ns	Total Numbers of Map Entries
MAP_EN(0)	Zeroth Entry Address
MAP_EN(1)	First Entry Address
MAP_EN(2)	Second Entry Address
MAP_EN(3)	Third Entry Address
MAP_EN(4)	Forth Entry Address
MAP_EN(5)	Fifth Entry Address
:	:
:	:
:	:
MAP_EN(MAX)	Final Entry Address

FIG.10


START
Initialize Retry Counter
S204
Instruct to Pickup Driving Means on Target Address
S205
S206
Optical Pickup Is onTarget Address ?
No
Yes
Instruct to Signal Processing Means on Error Correction
S207
S208
Error Correction Is Finished?
No
S211
Increment Retry Counter
Yes
S209
Buffering Is Finished ?
No
Yes
S212
Retry Times Exceed Upper Limit Times?
No
Yes
S210
Update to Target Address
Initialize Retry Counter
S213
Initialize MAP Reference Counter
S501
S502
What Is Number of MAP Reference Counter?
=0
S503
=1
S505
=2
S507
Update to Target Address
Update to Target Address
Update to Target Address
S504
Set MAP Reference Counter at 1
Set MAP Reference Counter at 2
Set MAP Reference Counter at 0
S506
S508
S214
Target Address Exceeds Final Target Address ?
No
Yes
END

TOSHIBA SCREENSHOT

FIG.11

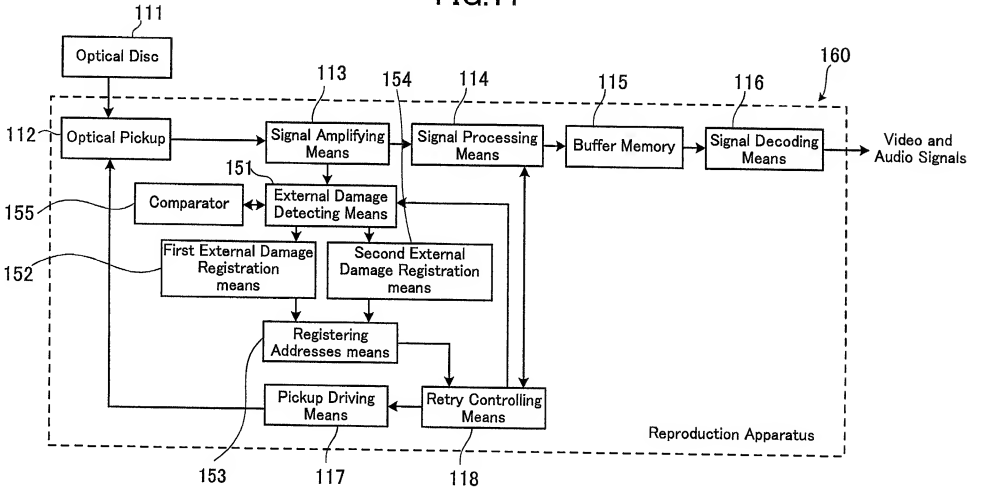


FIG.12

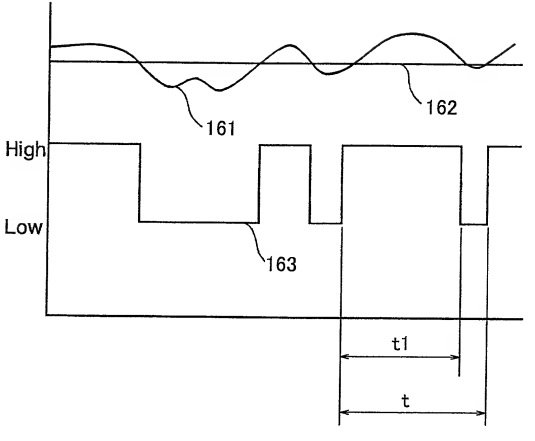
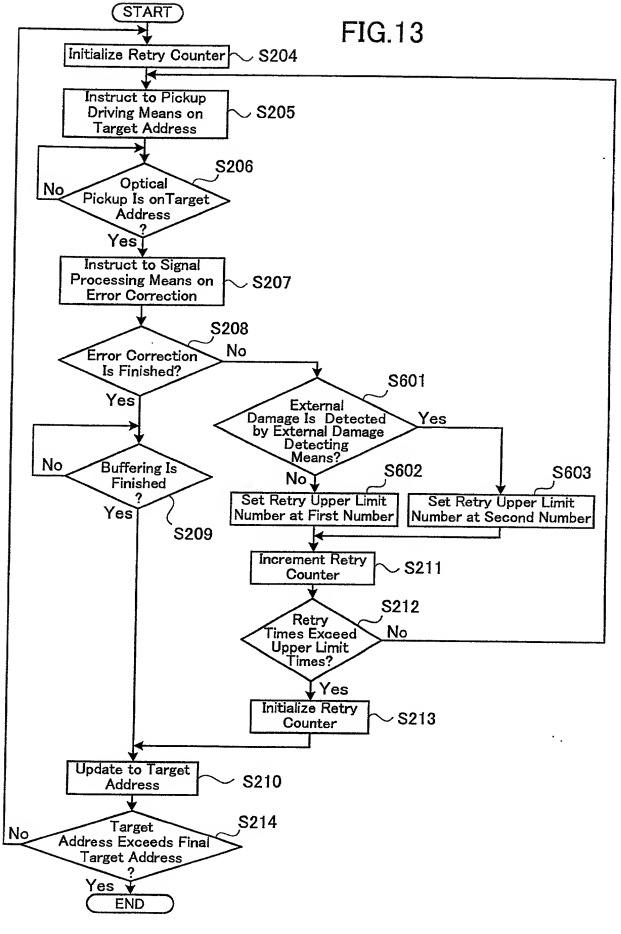


FIG.13


START
Initialize Retry Counter
S204
Instruct to Pickup Driving Means on Target Address
S205
Optical Pickup Is onTarget Address ?
S206
No
Yes
Instruct to Signal Processing Means on Error Correction
S207
Error Correction Is Finished?
S208
No
Yes
External Damage Is Detected by External Damage Detecting Means?
S601
Yes
No
Buffering Is Finished ?
No
Yes
S209
Set Retry Upper Limit Number at First Number
S602
Set Retry Upper Limit Number at Second Number
S603
Increment Retry Counter
S211
Retry Times Exceed Upper Limit Times?
S212
No
Yes
Initialize Retry Counter
S213
Update to Target Address
S210
Target Address Exceeds Final Target Address ?
S214
No
Yes
END

TOSHIBA EBS1007

FIG.14

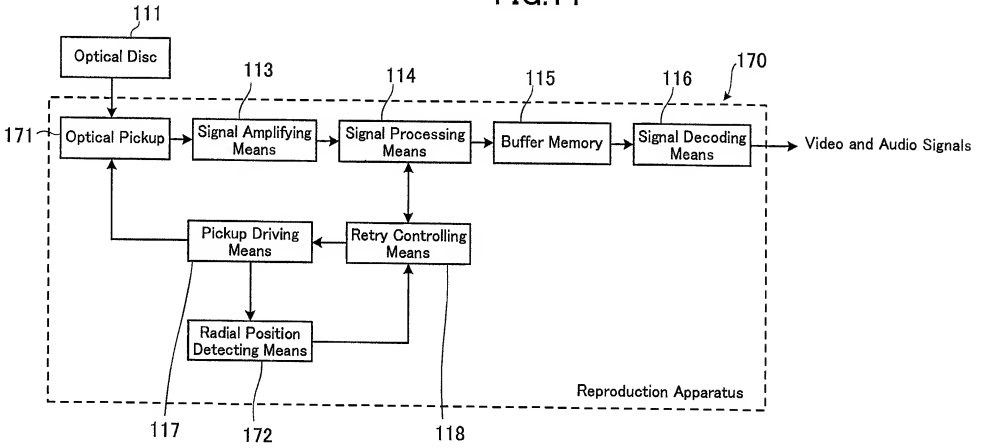


FIG.15

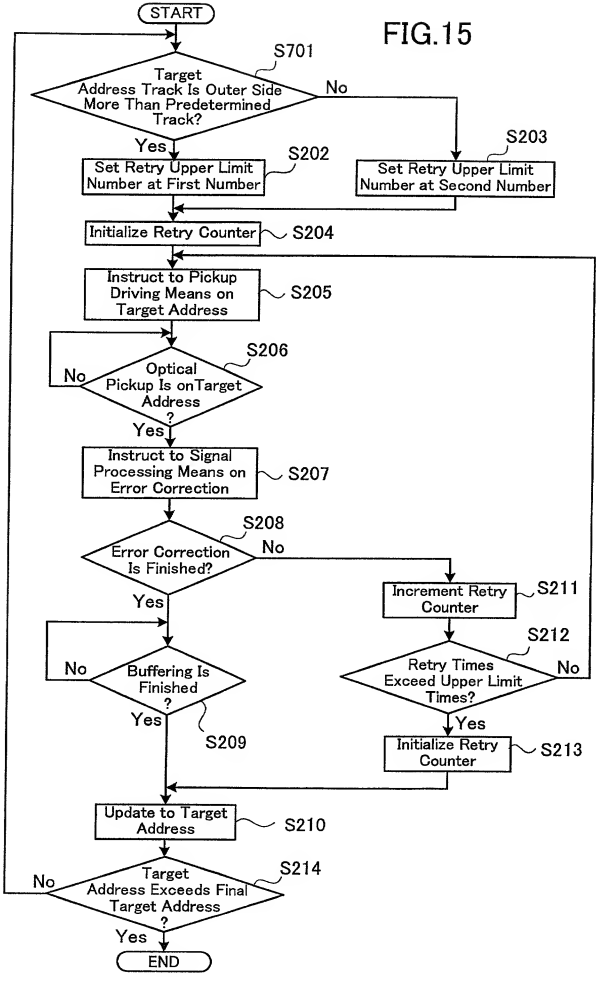

START
S701
Target Address Track Is Outer Side More Than Predetermined Track?
No
Yes
S202
Set Retry Upper Limit Number at First Number
S203
Set Retry Upper Limit Number at Second Number
Initialize Retry Counter
S204
Instruct to Pickup Driving Means on Target Address
S205
S206
No
Optical Pickup Is onTarget Address ?
Yes
Instruct to Signal Processing Means on Error Correction
S207
S208
Error Correction Is Finished?
No
Yes
Increment Retry Counter
S211
S212
No
Buffering Is Finished ?
Retry Times Exceed Upper Limit Times?
No
Yes
Yes
S209
Initialize Retry Counter
S213
Update to Target Address
S210
S214
No
Target Address Exceeds Final Target Address ?
Yes
END

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FIG.16

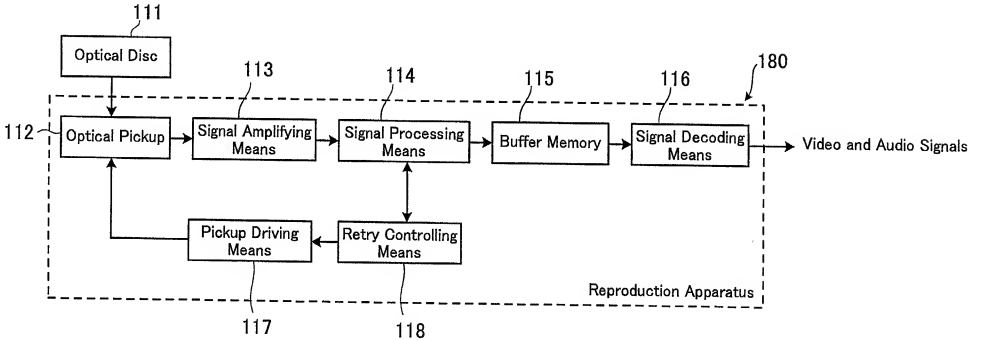
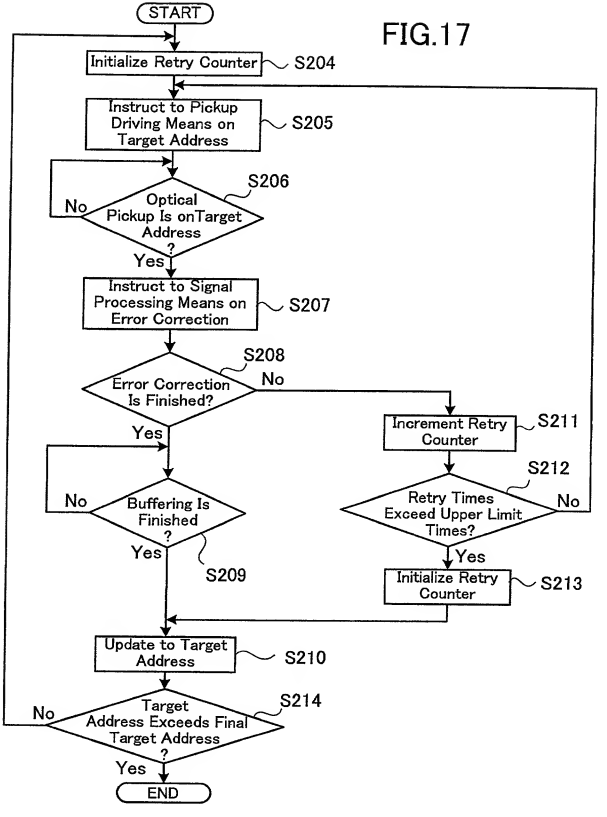


FIG.17


START
Initialize Retry Counter
S204
Instruct to Pickup Driving Means on Target Address
S205
S206
No
Optical Pickup Is onTarget Address ?
Yes
Instruct to Signal Processing Means on Error Correction
S207
S208
Error Correction Is Finished?
No
Yes
Increment Retry Counter
S211
S212
No
Buffering Is Finished ?
Retry Times Exceed Upper Limit Times?
No
Yes
S209
Yes
Initialize Retry Counter
S213
Update to Target Address
S210
S214
No
Target Address Exceeds Final Target Address ?
Yes
END